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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/712,067	11/13/2003	2811	770	0150114D	3	9	1

CONFIRMATION NO. 7928

Michael Farjami, Esq.  
 FARJAMI & FARJAMI LLP  
 16148 Sand Canyon  
 Irvine, CA 92618

## FILING RECEIPT



\*OC000000011862875\*

Date Mailed: 02/09/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

## Applicant(s)

Arjun Kar-Roy, Irvine, CA;  
 Marco Racanelli, Santa Ana, CA;  
 David Howard, Irvine, CA;

## Assignment For Published Patent Application

Newport Fab, LLC dba Jazz Semiconductors;

## Domestic Priority data as claimed by applicant

This application is a DIV of 10/410,937 04/09/2003 PAT 6,680,521

## Foreign Applications

If Required, Foreign Filing License Granted: 02/09/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

DOCKETED

FEB 17 2004

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## Title

Method for fabricating a <sup>high</sup> density composite MIM capacitor with reduced voltage dependence in

semiconductor dies

**Preliminary Class**

257

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**LICENSE FOR FOREIGN FILING UNDER  
Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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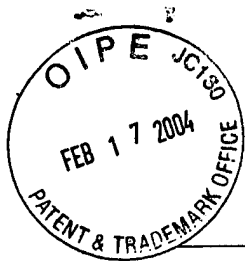
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kar-Roy, et al.

Serial No.: 10/712,067

Filed: November 13, 2003

For: **Method for Fabricating a High Density  
Composite MIM Capacitor with Reduced  
Voltage Dependence in Semiconductor Dies**

Art Unit: 2811

Examiner: Unknown

CORRECTION TO THE FILING RECEIPT

Initial Patent Examination's  
Filing Receipt Corrections  
Commissioner for Patents  
P.O. Box 1450  
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Dear Sir/Madam:

Enclosed please find a copy of the filing receipt with corrections noted thereon. As shown, please change title from "Method for Fabricating a Hig Density Composite MIM Capacitor with Reduced Voltage Dependence in Semiconductor Dies" to -- Method for Fabricating a High Density Composite MIM Capacitor with Reduced Voltage Dependence in Semiconductor Dies --.

Please call the undersigned if you have any questions.

Dated: 2/13/04

Respectfully Submitted;  
FARJAMI & FARJAMI LLP

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